

GE Intelligent Platforms

High
Performance
Embedded
Computing
(HPEC)



imagination at work



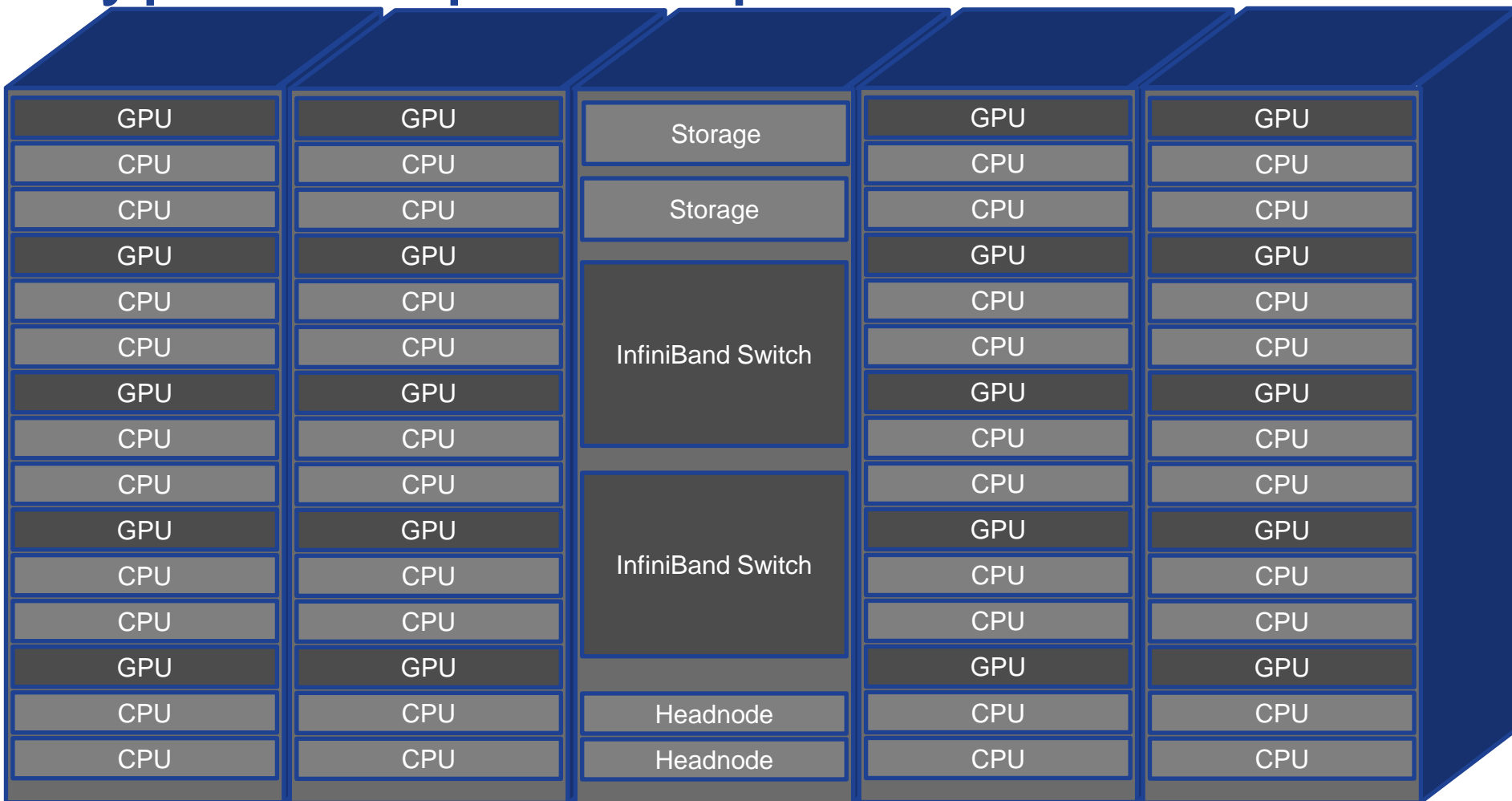
From Here

- Bringing technology from Supercomputing to rugged MilAero applications
 - Mainstream processors and interconnects
- Open Standards Architectures
 - Hardware
 - Software
- Dense High Performance Embedded Computing platforms
 - Lowest SWaP

To Here



Typical Supercomputer

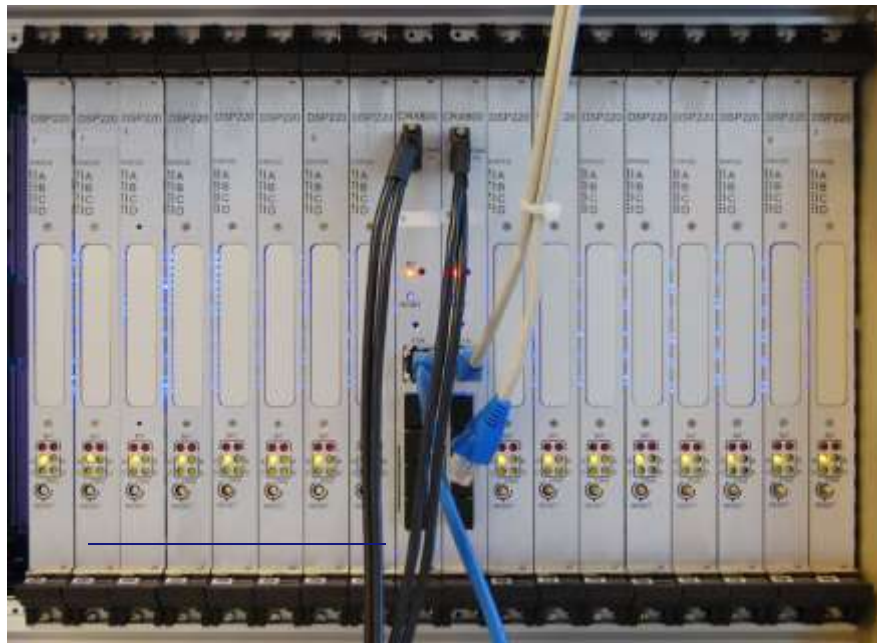


42% of the world's most powerful computers use InfiniBand and OFED
3 of the top 5 use GPGPU

Embedded Supercomputers



The same architectures, but designed for deployment in harsh environments



Shock,
Vibration,
Temperature extremes,
Long lifecycles

HPEC Center of Excellence (CoE) capabilities

Provide solutions based pre-sales customer engagement via HPEC capture team:

- System architecture definition.
- System performance studies & benchmarking.
- Application orientated system demos.

Develop 'application-ready' development systems:

- Develop integrated, pre-configured HPEC lab systems.
- Linux & middleware pre-installed, out-of-the-box examples & demos.
- Feed requirements into Systems team in Huntsville for volume production.

Provide post sales application support and professional services:

- On-site 'getting started' support & training.
- Application development support & services (custom algos etc).

GE supports HPEC from desktop to deployment

1

Desk Top PC or
Blade Servers



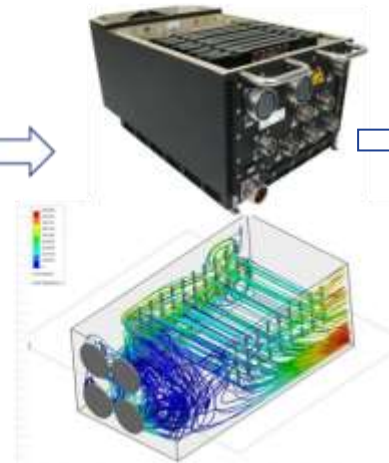
2

HPEC Application
Ready Platform
MOSA



3

Deployed Production
Platforms



HPC

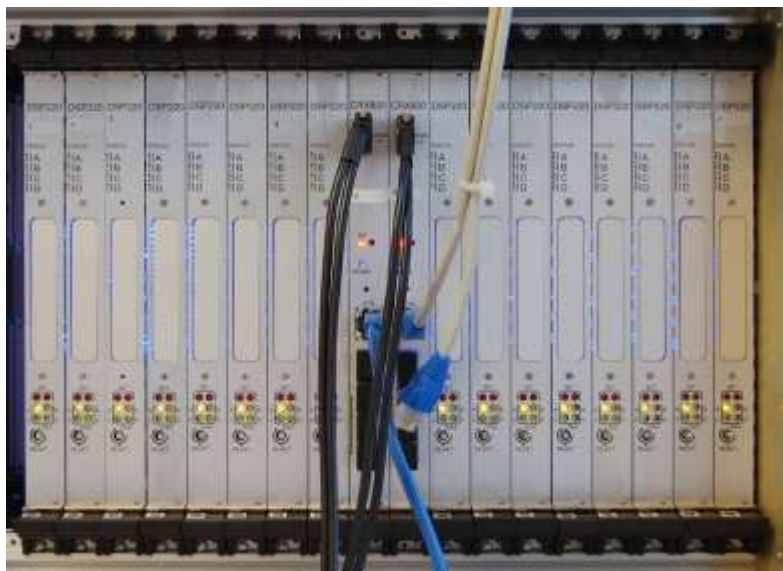
Benign

Deployable HPEC OpenVPX

Extended Temp.

Full rugged boards & systems

Radar processing



Typical older system:

4 cu ft., 105 lbs., 2000W

576 GFLOPS peak

(18 x quad PPCs, 6U VXS)



New system:

0.8 cu ft., 10 lbs., 120W

770 GFLOPS peak

SBC325 + GRA112

(Intel 3rd Gen i7 + Kepler GPGPU, 3U VPX)

The Enabling Technologies

Modular Open System Architecture

- *Modular OpenVPX platforms*
- *Latest Intel 2nd & 3rd Gen. Core i7, NVIDIA GPUs & Tileria Multicore*
- *10GbE, Infiniband & PCIe Switched Fabrics*

✓ Single Board Computers

✓ Multiprocessors

✓ GPGPU

✓ I/O Carriers

✓ Switch Modules



IPN250/251



SBC624/625



DSP280/281



NPN240/241

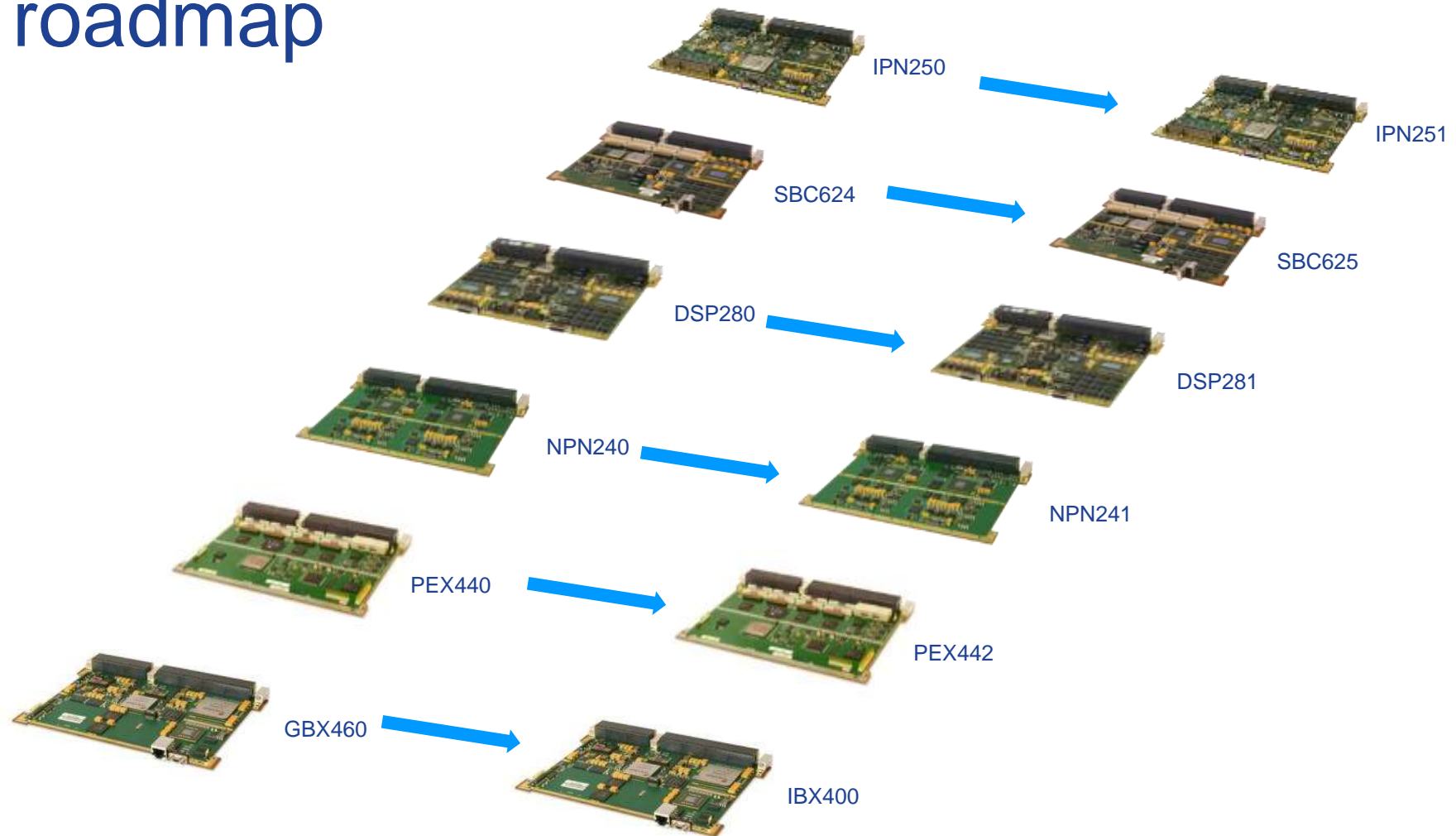


PEX442

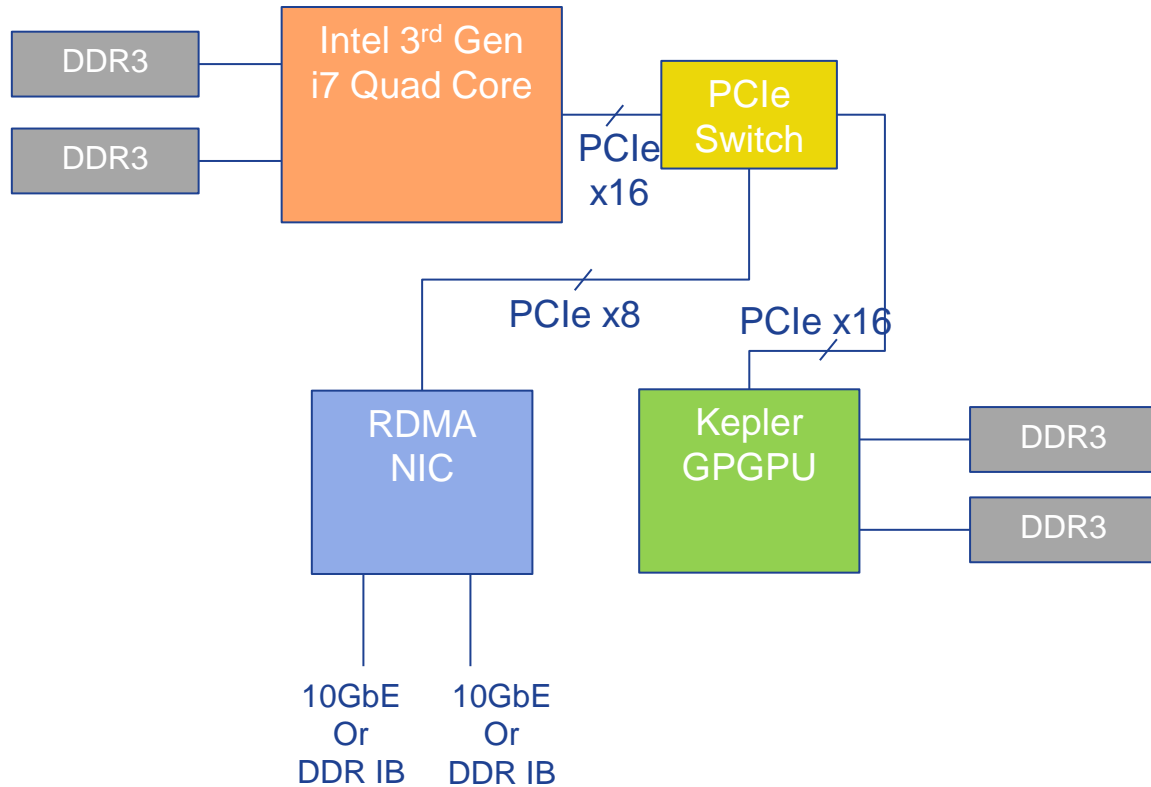


GBX460

MOSA components with technology roadmap



Embedded Compute Clusters

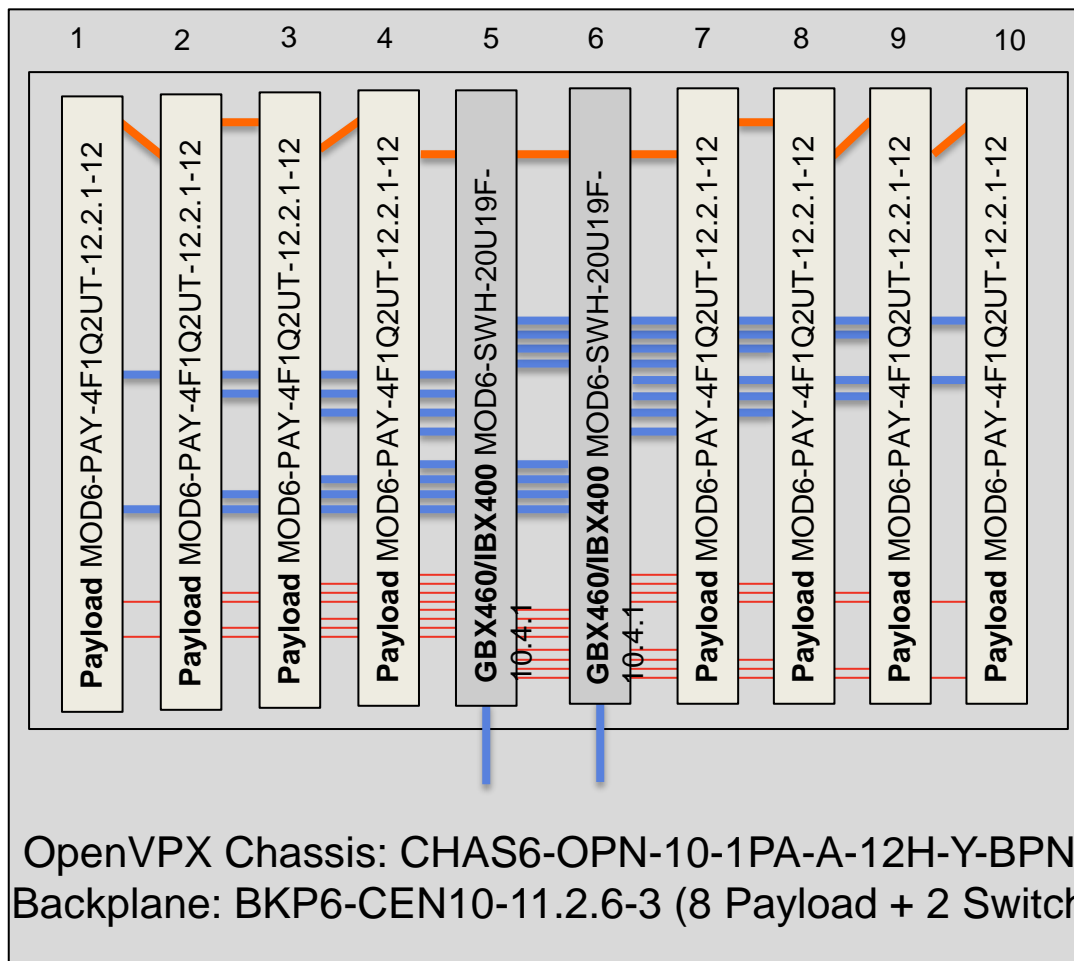


3rd Gen i7 ~ 150 GFLOPS
Kepler EXK107 ~ 622 GFLOPS

Cooling:
Air
Conduction
Spray
Air-flowthrough



OpenVPX enables fabric connections



Expansion Plane

– PCIe

Data Plane

– 10GbE,
InfiniBand

Control Plane

– 1GbE

AXIS

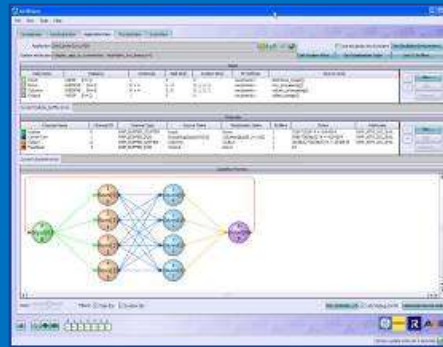
Advanced Multi-Processor Integrated Software

AXISLIB

$$\begin{aligned} \text{length} &= N \\ H[k] &= \sum_{j=0}^{N-1} x[j] e^{j \cdot (k - \lfloor M/2 \rfloor) \cdot 2\pi} \quad \text{for } 0 \leq k \leq N-1 \\ &\quad \lfloor M/2 \rfloor + 1 \leq k \leq \lfloor M/2 \rfloor \\ H[k] &= H[N-k] \cdot (-1)^{\lfloor M/2 \rfloor - k} \quad \text{for } \lfloor M/2 \rfloor + 1 \leq k \leq N-1 \\ &\quad \lfloor M/2 \rfloor + 1 \leq k \leq N-1 \\ \text{Example (see next page):} \\ \text{length} &= N - M + 1 \\ H[k] &= \sum_{j=0}^{N-1} x[j] e^{j \cdot k \cdot 2\pi} \quad \text{for } 0 \leq k \leq N-M \\ H[k] &= H[k] \cdot M \\ \text{View } |H[k]| &= \begin{cases} 1 & 0 \leq k < N \\ 0 & \text{otherwise} \end{cases} \end{aligned}$$

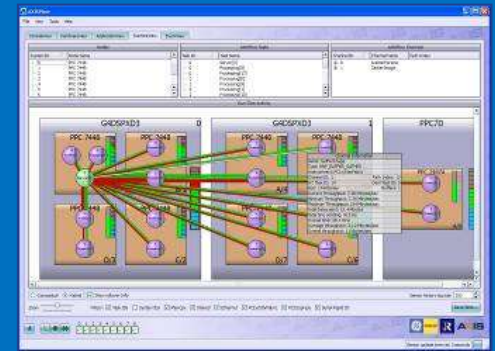
RSPL
VSIPL
VSIPL++

AXISFLOW



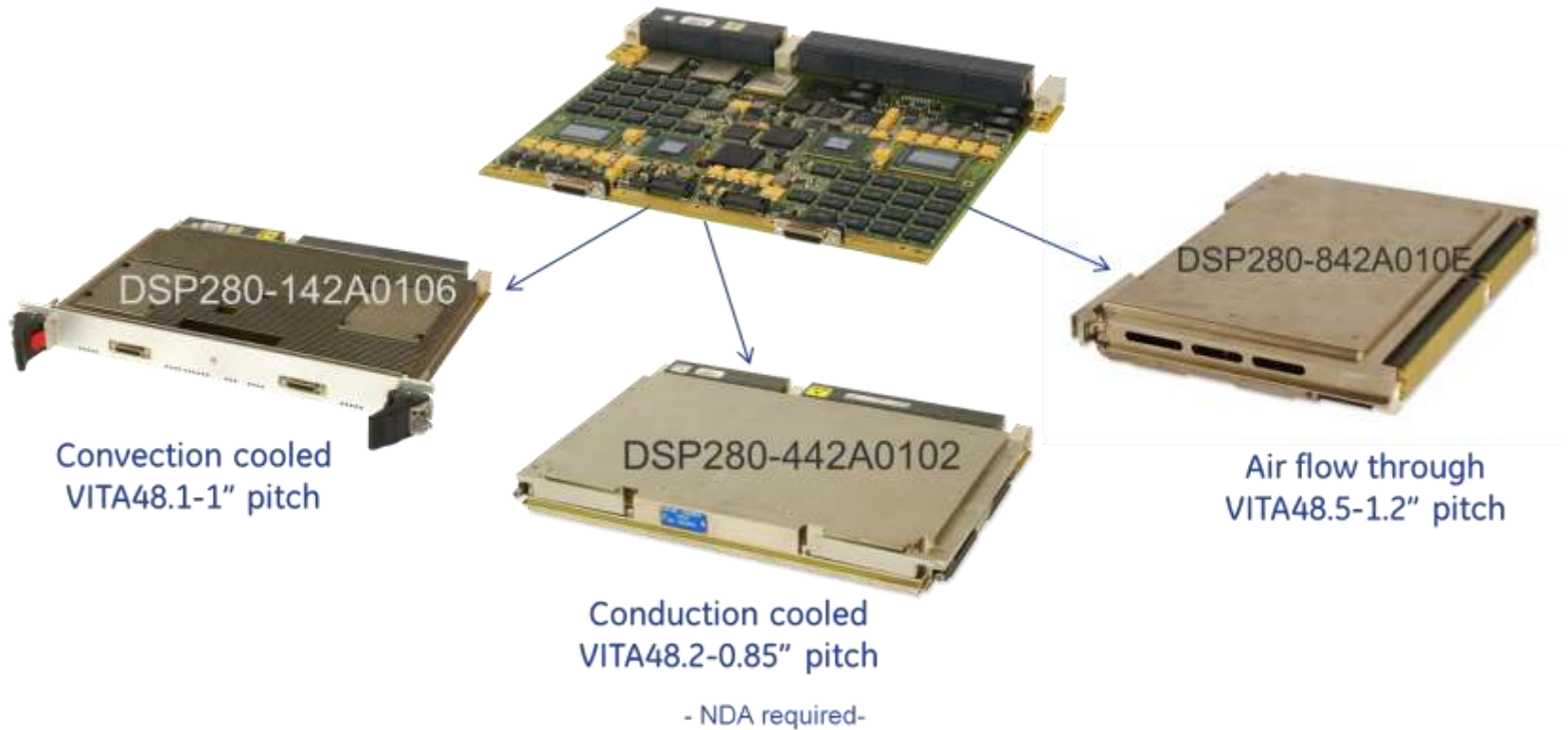
Dataflow
RDMA
MPI

AXISVIEW



Runtime
Performance
visualization
& tuning

Multiple OpenVPX Build Styles



Summary

Bringing technology from supercomputing to rugged MilAero applications

- Intel Processors
- NVIDIA GPGPUs
- InfiniBand and 10G Ethernet interconnects

Open Standards Architectures

- OpenVPX
- Linux
- VSIPL / VSIPL++
- OpenMPI / DDS / CORBA

Dense High Performance Embedded Computing platforms

- Lowest SWaP
- Air, conduction, air-flow through, spray, liquid cooling
- Fully rugged, Fully lifecycle supported

For more information visit: <http://defense.ge-ip.com/hpec>