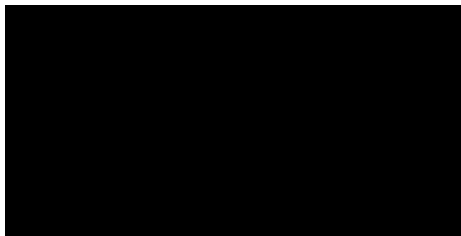
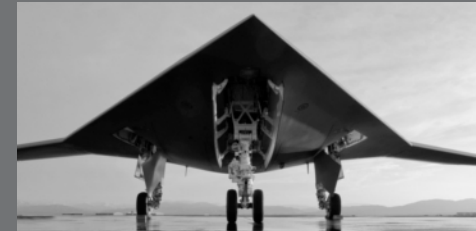


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WRIGHT**

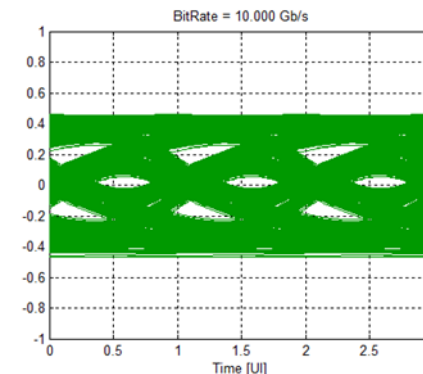
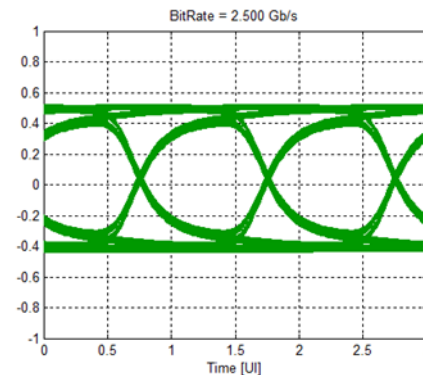
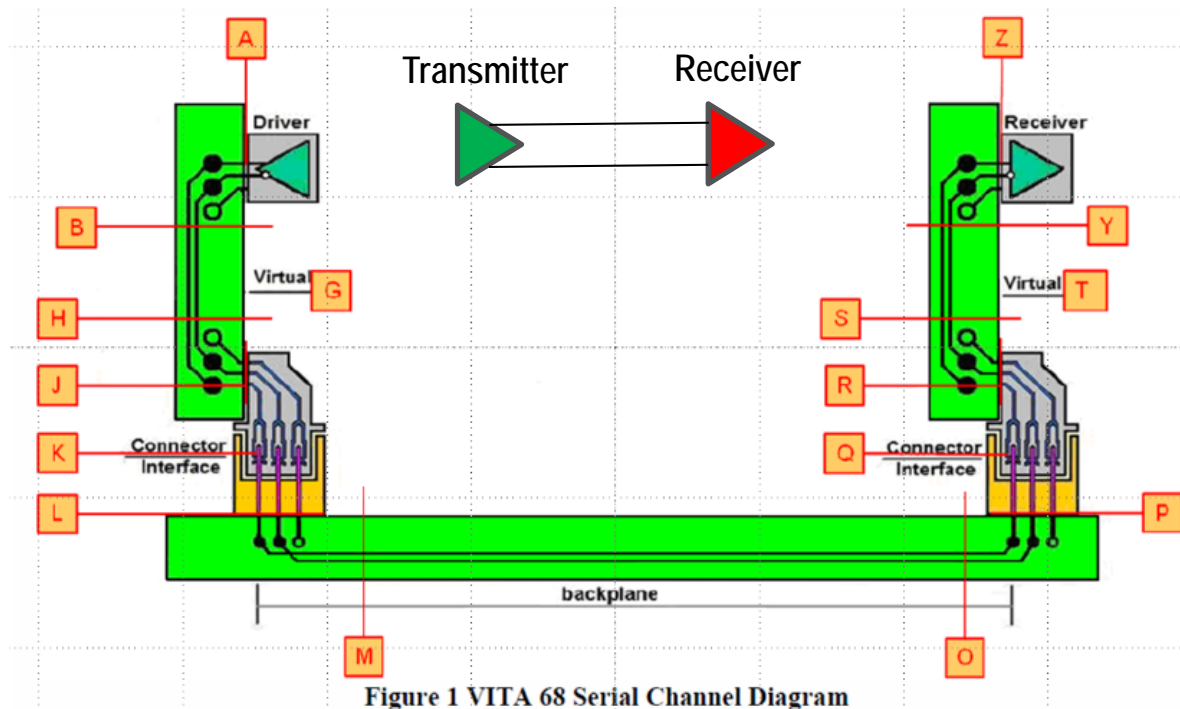
Defense Solutions Division



# High Speed VPX Signal Integrity (aka Hoodoo/Voodoo)

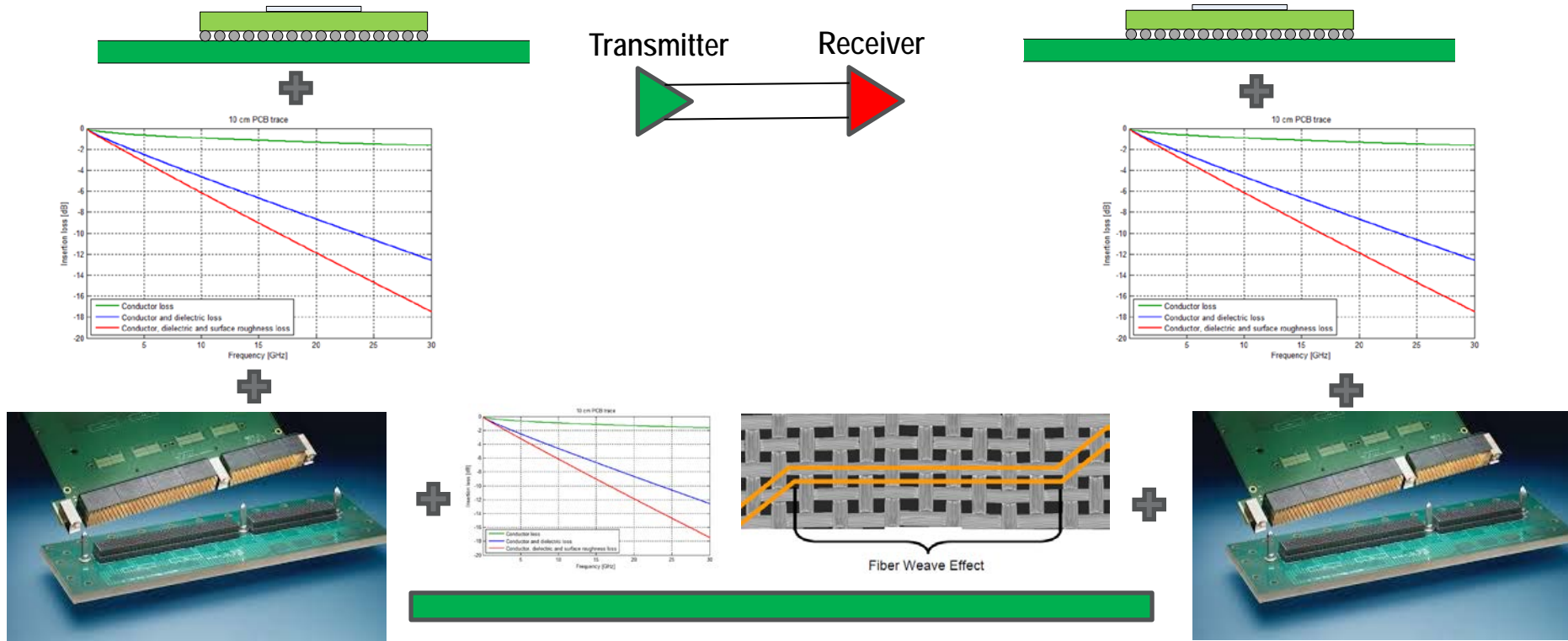


# Signal Integrity (SI)



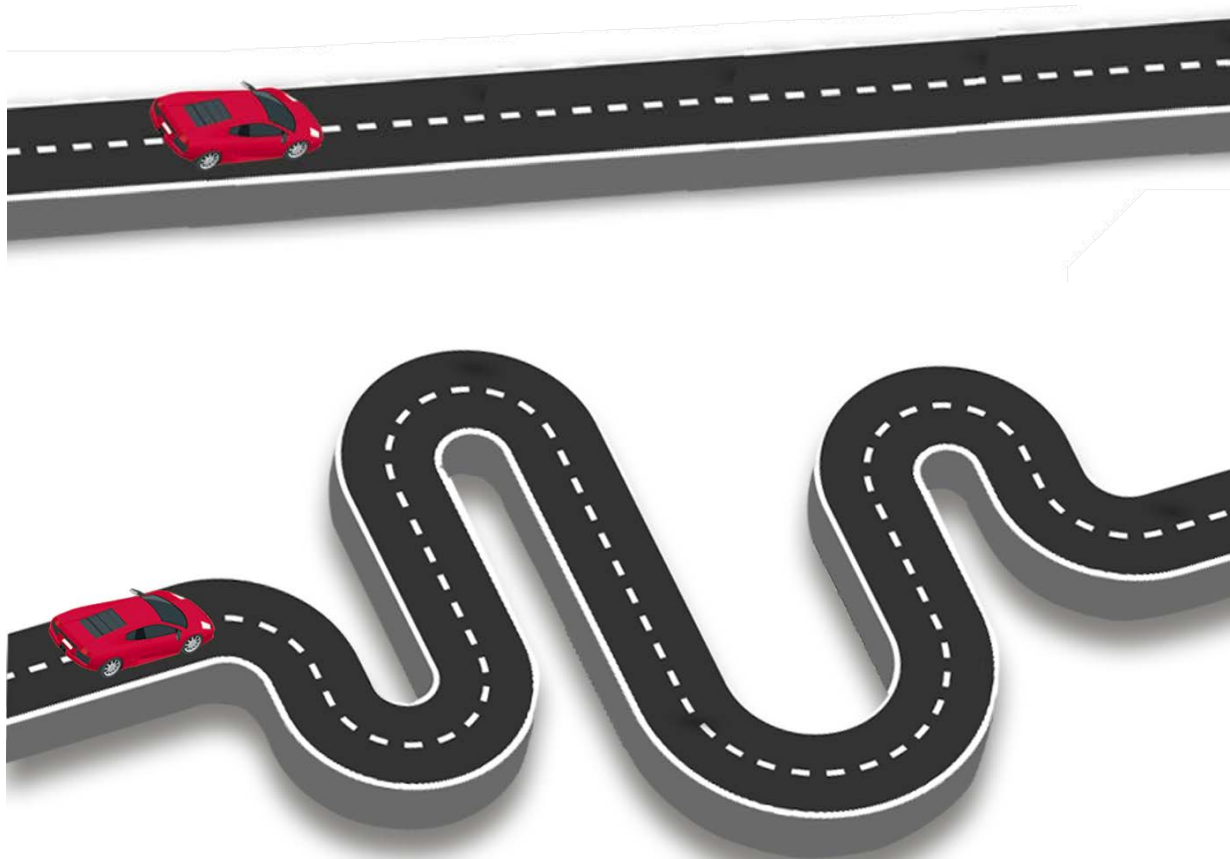
*Where's the Hoodoo/Voodoo?*

# Signal Integrity (SI)



*Where's the Hoodoo/Voodoo?*

# Signal Integrity - Good vs Bad





Courtesy: TE Connectivity



## Gen 1 Serial Fabrics, 2.5 – 3.125 Gbaud



Courtesy: TE Connectivity



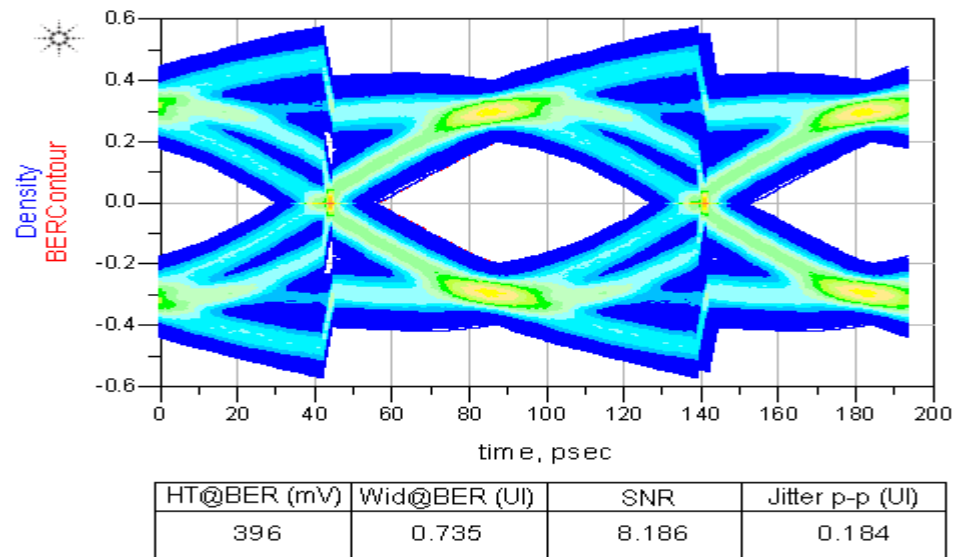
## Gen 2 Serial Fabrics, 5.0 – 6.25 Gbaud



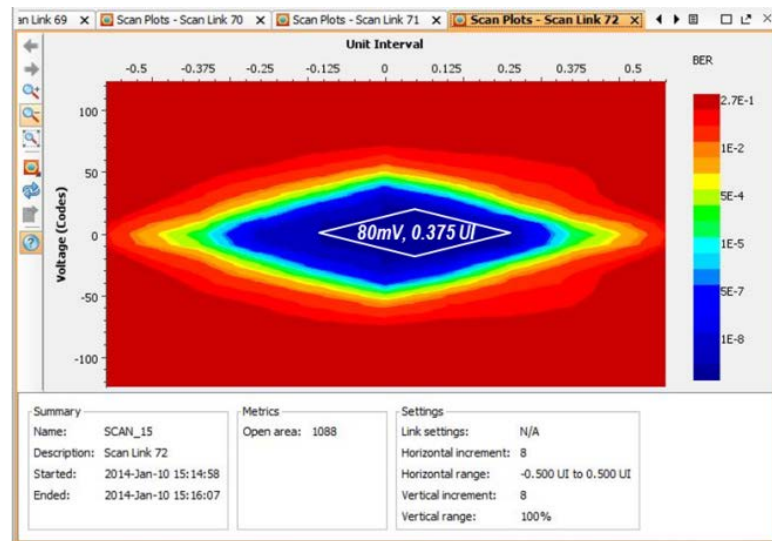
Courtesy: TE Connectivity



## Gen 3 Serial Fabrics, 8.0 – 10.3 Gbaud



Simulated Eye Diagram for worst channel at 807mV pk-pk TX



Tested Eye Diagram for worst channel with Ultra low power TX at only 250mV pk-pk

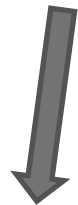
*Works with Significant Margin*



PCI EXPRESS®  
4.0



Gen 4 PCIe  
@ 16 Gbaud



FABRIC 64™



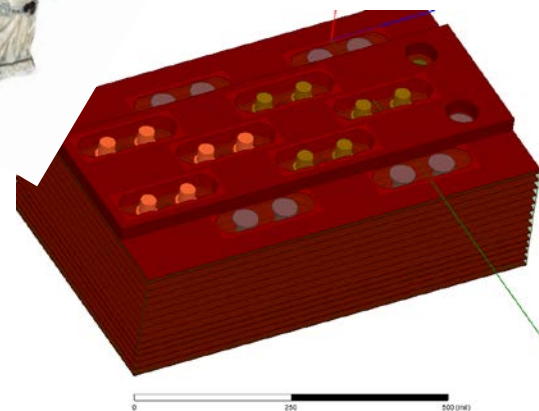
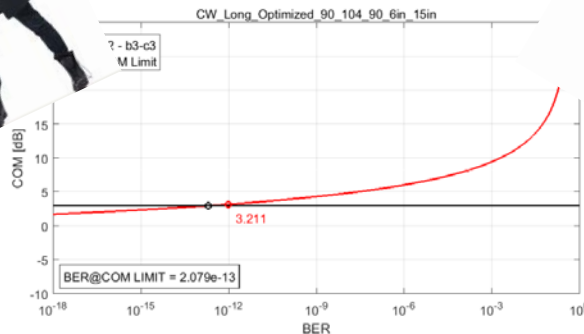
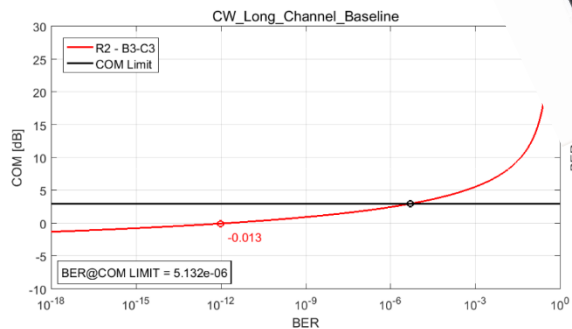
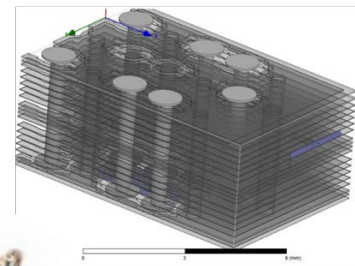
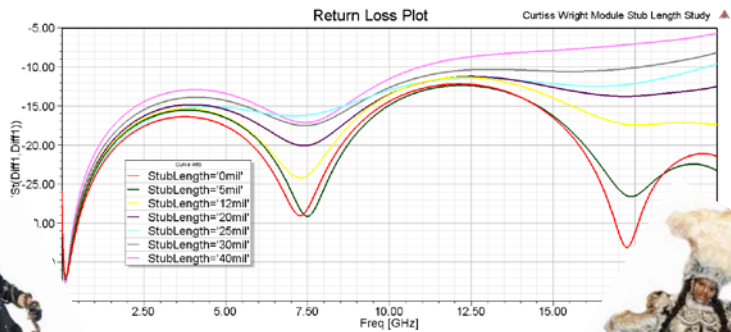
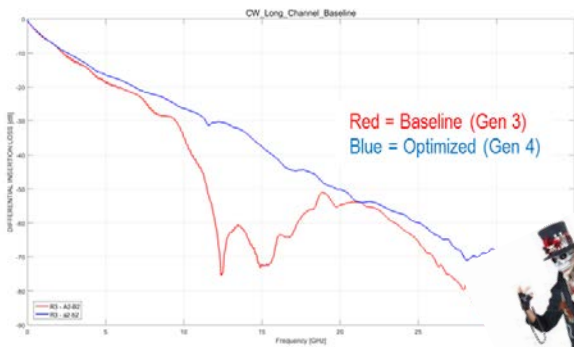
Courtesy: TE Connectivity



## Gen 4 Capability with Standard VPX

# Curtiss-Wright Gen 4 VPX/OpenVPX

F<sub>ABRIC</sub>64™

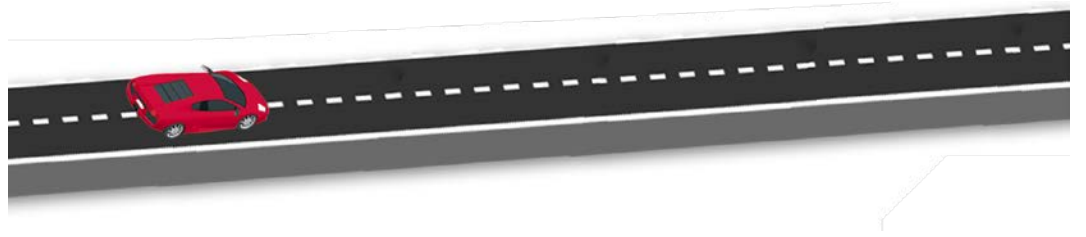


Optimized design (Gen 4) BER (bit error rate) is 7 orders of magnitude better than Gen 3 designs!

*Works with Margin!*

# What's next?

**F**ABRIC 100™



# The End

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*For more info contact: Ivan Straznicky  
ivan.straznicky@curtisswright.com*

**Q&A**

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